IN THE TITLE

Please delete the title in its entirety and insert — A DEVICE HAVING RECESSED SPACERS FOR IMPROVED SALICIDE RESISTANCE ON POLYSILICON GATES — therein.

IN THE CLAIMS

Please cancel claim 9 without prejudice.

Please amend the following claims:

8. (Amended) A gate electrode [formed on a substrate] comprising:

[an insulative layer formed on a substrate;]

a gate layer disposed above a substrate [formed on the insulative

layer];

[a conductive layer formed on the gate layer;]

thin first spacers <u>disposed</u> [formed] adjacent to opposite sides of <u>said</u> [the] gate layer, wherein <u>tops of said</u> [the] thin first spacers are [recessed] <u>at approximately the same height as top of said gate layer</u>; [and,]

thick second spacers <u>disposed</u> [formed] adjacent to each of <u>said</u> [the] thin first spacers, <u>said</u> thick second spacers having a flat upper surface, wherein <u>tops of said</u> [the] thick second spacers are [recessed] <u>at approximately the same height as top of said gate layer; and</u>

a conductive layer disposed on said gate layer, said conductive layer extending beyond edges of said gate layer.

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10. (Amended) The gate electrode of claim 9 wherein <u>said</u> [the] gate layer <u>comprises</u> [is a] polysilicon.

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- 11. (Amended) The gate ectrode of claim 10 wherein <u>said</u> [the] conductive layer <u>comprises</u> [is a] polycide.
- 12. (Amended) The gate electrode of claim 11 wherein <u>said</u> [the] thin first spacers <u>comprise</u> [are an] oxide.
- 13. (Amended) The gate electrode of claim 12 wherein <u>said</u> [the] thick second spacers <u>comprise</u> [are a] nitride.

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14. (Amended) The gate electrode of claim 13 wherein <u>said</u> [the] polycide <u>comprises</u> [is] titanium salicide (TiSi2).